

DC/DC Converter PN2-XXSXXANT Series

Typical Feature

- ◆ Fixed input voltage, isolated & unregulated, output power 2W
- ◆ Efficiency up to 86%
- ◆ Mini SMD package, international standard pin-out
- ◆ Isolation Voltage 1500VDC
- ◆ Operating Temperature -40°C to +105°C
- ◆ No load input current as low as 5mA
- ◆ Plastic case, flame class UL94 V-0



Test conditions: Unless otherwise specified, all parameter values had been tested at rated input voltage, pure resistive rated load, and at room temperature 25 °C.

Application Filed

It could be widely used for instrument, communication, pure digital circuit, general low frequency analog circuit, relay drive circuit, data exchange circuit, etc.

Typical Product List

Certificate	Part No.	Input Voltage Range (VDC)		Output Voltage/Current		Input Current (mA)Typ. @Rated Voltage		Max. Capacitive Load uF (Max)	Ripple & Noise 20MHz (mVp-p) Max/Typ	Efficiency (%) @full load/rated input	
		Rated	Range	Voltage (VDC)	Io (mA) Max / Min	Full load	No Load			Min	Typ
-	PN2-3V3S05ANT	3.3	2.97 - 3.63	5	400/40	739	20	2400	100/50	79	82
CE	PN2-05S3V3ANT	5	4.5 - 5.5	3.3	400/40	500	5	2400	100/50	77	80
CE	PN2-05S05ANT			5	400/40	476	5	2400	100/50	80	83
CE	PN2-05S07ANT			7	285/28	465	10	1000	100/50	80	83
CE	PN2-05S09ANT			9	222/22	465	10	1000	100/80	82	85
CE	PN2-05S12ANT			12	167/17	455	20	560	100/80	83	86
CE	PN2-05S15ANT			15	133/13	455	20	560	100/80	79	82
CE, RoHS	PN2-12S05ANT	12	10.8	5	400/40	200	8	2400	100/80	81	84
CE, RoHS	PN2-12S12ANT		-	12	167/17	190	8	560	100/80	83	86
CE, RoHS	PN2-12S24ANT		13.2	24	83/8	185	10	470	150/80	81	84
-	PN2-15S05ANT	15	13.5	5	400/40	160	10	2400	100/80	79	82
-	PN2-15S12ANT		-	12	167/17	158	10	560	100/80	81	84
-	PN2-15S15ANT		16.5	15	133/13	156	10	560	100/80	81	84
-	PN2-24S05ANT	24	21.6 - 26.4	5	400/40	100	8	2400	100/80	81	84
-	PN2-24S5V5ANT			5.5	363/36	100	8	2400	100/80	81	84
-	PN2-24S12ANT			12	167/17	98	8	560	100/80	83	86
-	PN2-24S24ANT			24	83/8	96	8	470	150/80	81	84

Note: The ripple and noise are tested by the twisted pair method.

Input Specifications

Item	Operating Condition	Min.	Typ.	Max.	Unit
Input inrush voltage (1Second Max.)	3.3Vdc Input	-0.7	--	7	Vdc
	5Vdc Input	-0.7	--	9	
	9Vdc Input	-0.7	--	12	
	12Vdc Input	-0.7	--	18	
	15Vdc Input	-0.7	--	21	
	24Vdc Input	-0.7	--	30	
Input Filter Type	Capacitor Filter				
Hot Plug	Unavailable				

Output Specifications

Item	Operating Condition	Min.	Typ.	Max.	Unit	
Output Power		0.2	--	2	W	
Output Voltage Accuracy	Please refer to the output voltage deviation curve (Figure 1)					
Load Regulation	10%-100% load	3.3Vdc output	-	15	20	%
		Other voltage output	-	10	15	
Line Regulation	Input voltage change $\pm 1\%$	3.3Vdc output	-	-	1.5	--
		Other voltage output	-	-	1.2	
Temperature Drift Coefficient	Full load	-	-	± 0.03	%/°C	
Short Circuit Protection	Continuous, Self-recovery					

General Specifications

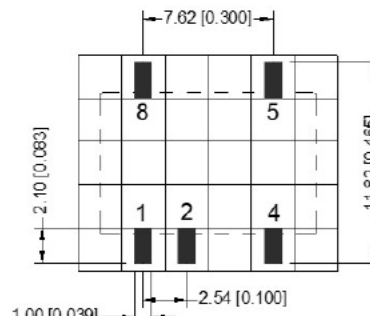
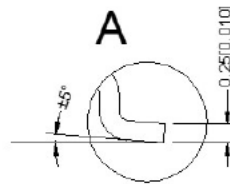
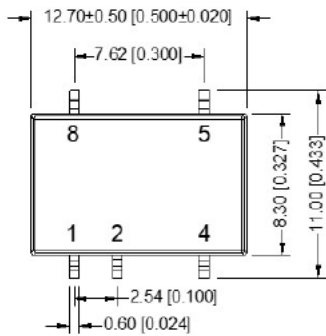
Item	Operating Condition	Min.	Typ.	Max.	Unit
Switching Frequency	Rated input voltage, full load	--	260	--	KHz
Operating Temperature	Refer to the temperature derating curve (Figure 2)	-40	--	+105	°C
Storage Temperature		-55	--	+125	
Case Temperature Rise	Operating at Ta =25°C	--	30	--	
Pin Soldering Temperature	1.5mm from the case, 10S	--	--	300	
Reflow Temperature	Peak temperature Tc $\leq 250^\circ\text{C}$, the maximum time above 217°C is 60S				
Relative Humidity	No condensing	5	--	95	%RH
Isolation Voltage	Input-Output, test 1min, leakage current <1mA	1500	--	--	VDC
Insulation Resistance	Input-Output, @ 500Vdc	1000	--	--	MΩ
Isolation Capacitor	Input/Output, 100KHz/0.1V	--	20	--	pF
MTBF	MIL-HDBK-217F@25°C	3500	--	--	K hours
Case Material	Plastic in Black, flame class UL94 V-0				
Product Weight	1.4 g (Typ.)				

Cooling Method	Natural air		
Package Size	L x W x H	12.70X11.00X7.13 mm	0.500 × 0.433 × 0.281 inch

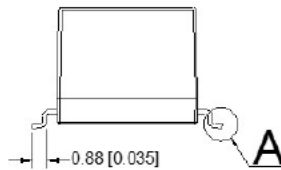
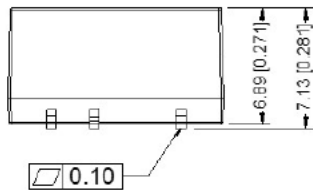
EMC Characteristic

EMI	CE	CISPR32/EN55032 CLASS B (with EMC Recommended Circuit)
	RE	CISPR32/EN55032 CLASS B (with EMC Recommended Circuit)
EMS	ESD	IEC/EN61000-4-2 Air ±8kV, Contact ±6kV perf. Criteria B

Mechanical Dimensions



Grid distance 2.54X2.54mm



Pin function	
No.	Function
1	-Vin
2	+Vin
4	GND
5	+Vo
8	NC

NC should not connect to any circuit

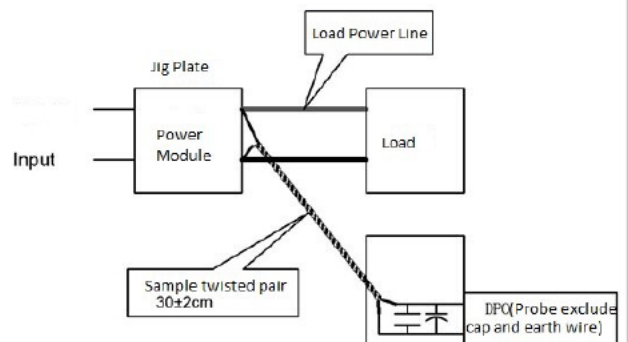
Note:
Unit: mm[inch]
Pin section tolerance: ±0.10[0.004]
General tolerance: ±0.25[0.010]

Note: Please take the pin definition on the product label as the right one which is different than the one defined in this data sheet.

Ripple & Noise Test Instructions (Twisted Pair Method, 20MHz Bandwidth)

Test Method:

- 1) Ripple noise test need 12# twisted pair cables, an oscilloscope which bandwidth should be set to 20MHz, 0.1uF polypropylene capacitor and 10uF high-frequency low-resistance electrolytic capacitor are connected in parallel with the probes (100M bandwidth). The oscilloscope should be set at the Sample Mode.
- 2) The output ripple noise test diagram is shown on the right. The converter output connects to the electronic load by the jig with cables which size should be defined according to the output current value. The twisted pair (length 30cm±2 cm) should be connected in parallel with the load, the location is as close as possible to the output pins or terminals. The test can be started after input power on.



Product Performance Curve

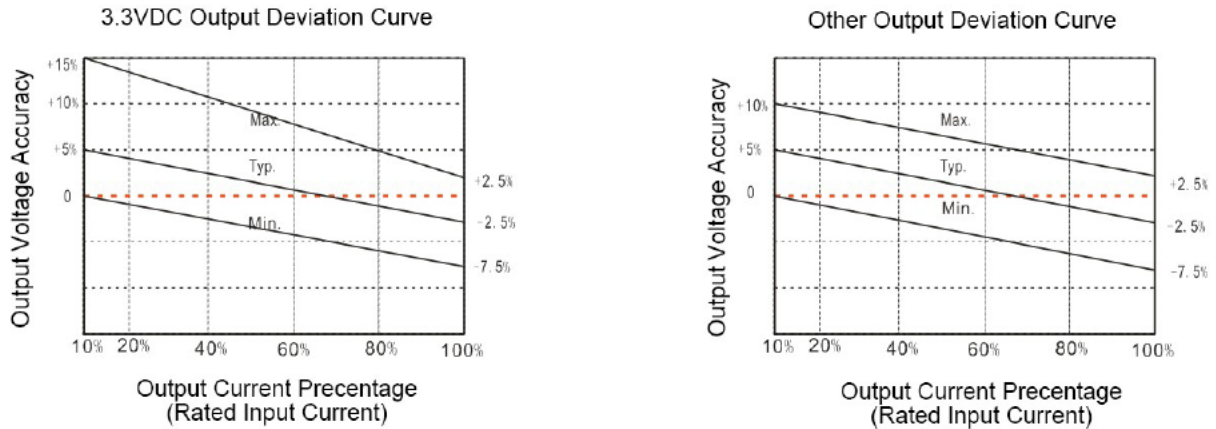


Figure 1

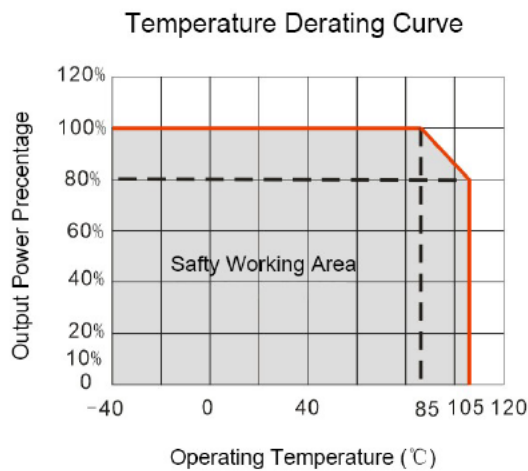


Figure 2

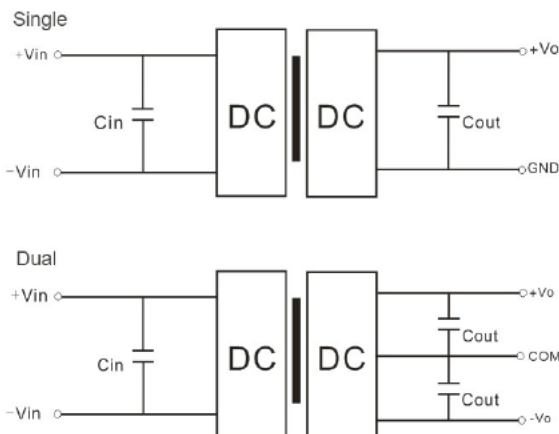
Recommended Circuits for Application

① Output load requirements

The maximum capacitive load of the product was tested at the Rated full load. The converter may not start or be damaged if the output capacitor exceeds this value.

② Recommended circuit for application

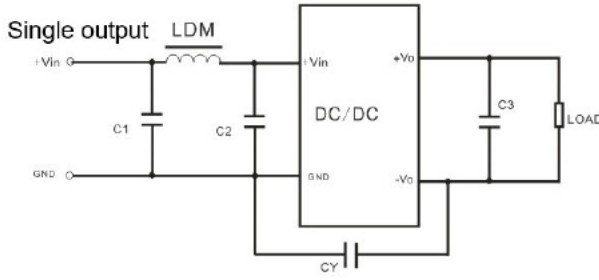
To effectively decrease the input and output ripple and noise, a capacitor filter should be connected at the input and output, the application circuit is shown in the figure below. The suitable filter capacitors should be chosen as the recommended capacitive load values in Table 1. The converter could not start if the capacitance is too big.



Recommended Capacitive Load Value Table (Table 1)

V _{in} (V _{dc})	C _{in}	Single V _{out} (V _{dc})	C _{out} (μF)	Dual V _{out} (V _{dc})	C _{out} (μF)
5	10 μF/16V	3.3	10 μF/16V	±3.3	4.7 μF/16V
12	2.2 μF/25V	5	10 μF/16V	±5	4.7 μF/16V
15	2.2 μF/25V	9	2.2 μF/25V	±9	2.2 μF/25V
24	1 μF/50V	12	2.2 μF/25V	±12	1 μF/25V
--	--	15	1 μF/25V	±15	1 μF/16V
--	--	24	1 μF/50V	±24	0.47 μF/50V

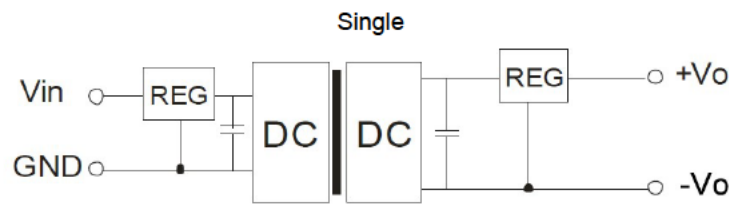
③ Recommended EMC Circuit



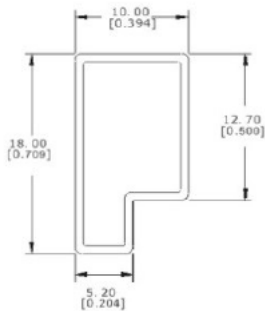
Input voltage		5VDC	12/15/24VDC
EMI	C1/C2	4.7 μ F/16V	4.7 μ F/50V
	CY	270pF/2KV	270pF/2KV
	C3	Refer to Cout in Table 1	
	LDM	6.8 μ H	6.8 μ H

④ Output voltage regulation and over voltage protection

The simple solution to achieve the output voltage regulated, over voltage and over current protections is to connect a linear regulator with overheat protection at input or output, and a capacitor filter connected in parallel as below circuit. Filter capacitive value recommended see table 1, Linear regulator should be chosen according to the actual voltage & current for operating. Or Aipu NW series products are recommended instead.

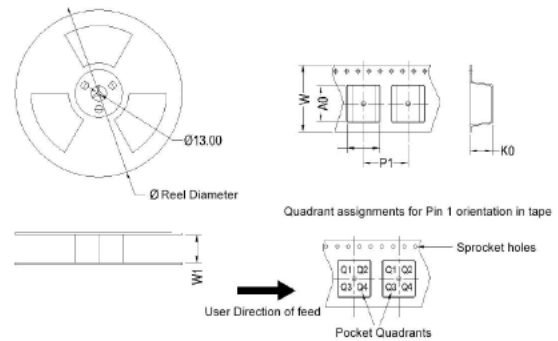


Packing



Note:
Unit: mm [inch]
General tolerance: $\pm 1.50[\pm 0.059]$
Quantity per tube: 39pcs
Quantity per Carton: 3120pcs
Tube size: 525x18x10mm
Carton size: 542x110x155mm

Tube packing



Device	Package Type	Pin	SPO	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
PN2-XXSXXANT	SMD	5	500	330	24.5	13.1	11.7	7.5	18.0	24	Q1

Reel packing (500pcs per Reel)

Application Notice

- 1.This product cannot be used in parallel, and does not support hot-plugging.
- 2.The product performance in this manual cannot be guaranteed if it works at a lower load than the minimum load condition.
3. All values or indicators in this manual had been tested based on Aipupower test specifications.